Varun Shekhar

**Mobile:** 9743946297 (Bangalore).  ***varunshekharrc92@gmail.com***

Seeking a challenging position that requires a highly motivated, sincere and technically strong engineer. Team player with engineering technical skills, motivation, and focus along with ability and confidence to deliver on projects.

**PROFILE:**

* Good academic knowledge on basic concepts of telecommunication.
* Demonstrated exposure in full life-cycle implementations with skills in executing Software Development Life Cycle (SDLC) and Document Development Life Cycle (DDLC) in compliance to quality standards.
* Excellent written and oral communication skills .
* Willing to learn new skills and work hard to achieve the company’s goals.

**Employee experience:**

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| **Company name** | **title** | **Employment period** | **Project details** |
| Alcatel-lucent | Graduate Trainee Engineer | 7 months | * Worked as an information developer in the project for 5520 AMS product and 5450 ISC product. * I was mainly involved in presenting and making the documentation team understand the core technical aspects of the product. * Other responsibilities include GUI level testing with good knowledge about the product’s architecture and JIRA tracking tool.   **Academic Projects**:  “RF Energy Harvesting using Microstrip Patch Antenna”, 2014.   * This project investigated the possibility of using available RF energy to charge batteries wirelessly, with the potential to apply to technology to the expected growth of devices in the coming “Internet of Things” boom. * The circuit is designed so that it can be harnessed by low power devices. * Miniaturization of the circuit design promotes implementation in wrist watches. * ADS simulation tool by Agilent Technologies has been used for designing the circuit and to obtain simulation results.   **KVPY Project- IISc**  “A Study on Time dependent voltage in fruit acid batteries”, guidance of IISc, Bangalore, 2011-‘12   * This project investigated the potential of the simple to design, cheap and bio-friendly, fruit acid batteries, as a source of power for operating today’s innumerable low power electronic devices. * Project was selected as one of India’s top 60 projects (years 2011-12). |

**Technical Skills:**

* **Areas of interest**: Wireless Telecommunication, Cloud Computing, Big Data, VLSI, Logic design, Embedded systems, integrated circuit design.
* **Programming language:** C, Verilog
* **Computer Tools**: Simulation tools like P-spice, Cadence, ADS. Authoring tools like Arbortext, FrameMaker.
* **Operating systems:** Linux, Windows7, Windows XP.
* **Networking:** OSI Model, LAN, WLAN, Subnetting, VLSM, VLANS, Inter VLAN routing, DNS, DHCP.

**Academic qualifications:**

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| **Bachelor of Engineering (B.E)**, SVIT College, VTU, Bangalore   * Passed with 82.52% (DISTINCTION)-July 2014 * Branch: Electronics and Communication | **Pre-University**: RV Pre-University College, Bangalore   * Secured 91.83% at State Board Exams | **SSLC**: Poorna Prajna High School  Belur, Karnataka, India   * Secured 96.8%, Karnataka Secondary Education Examination board. * District Award Winner – Highest marks |

**Key Achievements:**

* Secured University wide 11th rank for Visvesvaraya Technological University (VTU), Final semester, ECE branch. College topper two successive years (2010-11 and 2011-12).
* Nominated for the Commonwealth Scholarship and Fellowship Plan – 2015 – U.K. Selected for Government of India’s Ministry of Human Resource Development (**MHRD)** scholarship (2010-2014).
* Main event coordinator on Robotics workshops conducted by Tech-Bharat in collaboration with IIT-Delhi (2013). Main event coordinator for the State Level quiz competition (2013).
* Successfully completed a certified course on Cryptography from Stanford University from Coursera.
* Engineering project selected by Indian Institute of Science (IISc) as one of India’s top 60 projects (2011-12).
* Paper presentation: *RF Energy Harvesting* at a National Conference on Technological Advances in Electronic System Design (2014).

**Personal Details:**

**Date of Birth:** 13-05-1992

**Father’s Name**: R S Chandrashekhar

**Mother’s Name:** Latha Shekhar

**Permanent Address**: Belur, Karnataka, India

**Languages Known:** English, Kannada, and Hindi.

**Marital Status**:Unmarried.

I hereby declare that the information furnished above is true to the best of my knowledge.

**Date:** 10 March, 2016

**Place:** Bangalore (VARUN SHEKHAR R C)